



Workshop
on
**MEMS Integration
with
Electronic Circuits**

ORGANIZED BY

Centre for Incubation, Innovation, Research and Consultancy (CIIRC) &
Dept. of CSE, Jyothy Institute of Technology, Bangalore
in association with
Institute of Smart Structures and Systems (ISSS)

Date: 4th - 6th July 2023

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About CIIRC

CIIRC® is a multidisciplinary research, innovation, and incubation centre and a joint initiative by Sri Sharadha Peetham, Sringeri, and Jyothy Institute of Technology (JIT). It is the centralized R&D centre of JIT besides being recognized by DSIR as a Scientific and Industrial Research Organisation (SIRO) centre with DSIR certification. CIIRC® has emerged with a promise of ushering a new philosophy by bringing, for the first time on the same platform, diverse components of science, engineering, business orientation and skill development.

About JIT

Jyothy Institute of Technology is affiliated to Visvesvaraya Technological University and is approved by AICTE, New Delhi. JIT was established in the year 2011 under the leadership of Karnataka Rajyothsava Awardee, Dharmika Pravara, Late Dr. B.N.V Subrahmanya, visionary par excellence. The institution hosts more than 35 doctorates and the rest being masters.

About ISSS

ISSS is a Professional Society that helps its members to keep pace with the advances being made and gives impetus to the emerging Smart technology in India. It organizes an international conference every three years on Smart Materials, Structures and MEMS, which has helped to bridge the gap between the advanced level of technology abroad and the R&D effort in India. In addition, it organizes and supports various national conferences and awareness workshops in this area. It also provides a common forum for the scientists and researchers working in various inter-related fields. The basic aim of this Society is to foster dedicated research among Indian scientists in this vital, state-of-the art and cutting-edge technology. This Society formed by a core group of scientists and experts has access to infrastructure and scientific support of all DRDO, CSIR, DAE, DOS Laboratories, and Academic Institutions in various cities of the country.

About the Workshop

Micro-Electro-Mechanical-Systems (MEMS) sensors are widely popular for their sensitivity. It is important to read the signals provided by these sensors. In this workshop (Level 0) we will learn:

- Design and simulation of piezo-resistive MEMS pressure sensor
- Design and simulation of interfacing circuit for MEMS sensor

Design and simulation of MEMS devices and electronic circuits will be done using IntelliSuite and LTSPICE tools, respectively. There is no prerequisites to attend the workshop. It will be useful for UG/PG students, faculties and Research Scholars working in the areas of sensors. The content of the workshop is delivered through pre-recorded videos. It will be moderated to clarify all questions during the workshop. There will be a follow-up sessions two weeks after the workshop.

Registration Fee

Students – Rs.2500
Faculties – Rs.3000

Registration



Scan via Browser

Payment



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