PMRF-ISSS Teaching Programme

Prime Minister Research Fellowship students' teaching requirement facilitated by the Institute of Smart Structures and Systems

Module **PMRF-ISSS173**

Introduction to RISC-V Processor

Name of the PMRF student

Krishna Sai Tarun Ramapragada

Required background of the students taught

- UG Students of Electronics Engineering, Electrical Engineering

Details of the content of the module

Module 1: (~ 10 hours)
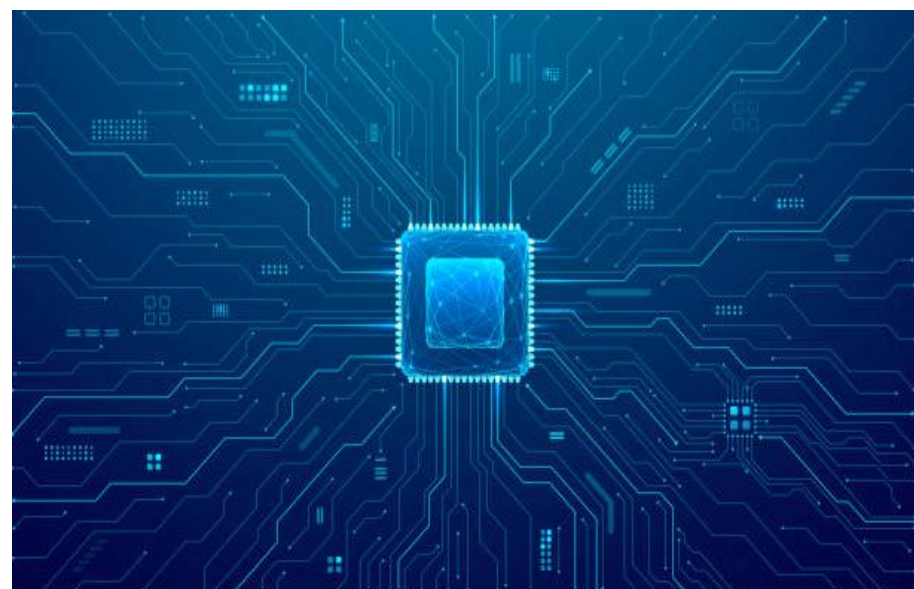**Arithmetic circuits and Memory Basics**

Module 2: (~ 7 hours)
RISC V addressing and RISC V ISA

Module 3: (~ 6 hours)
RISC V Single Cycle Processor Design

Module 4: (~ 4 hours)
Pipelining Basics and Conclusion

Schedule of the module

Start Date: Dec 15, 2024

End Date: June 15, 2025 (Tentative)

Total Duration: 27 – 29 hours

Class Schedule: Every Sunday

Class Timings: 11:00 am to 12:00 pm IST (1 hour)

Meeting link : Will be shared later

Link

Contact email ID: isss.forum@gmail.com

Registration link:
https://forms.gle/3Hh57p1sqGtyuFWr5